

Abstract

A memory card connector which comprises a header and a carrier connected to the header, wherein the header is provided with a plurality of pins extended in both inward and outward directions. The inward pins are used for inserting a memory card to be received in the carrier. A vertical circuit board is electrically and securely connected to the outward pins of the header. The lower end of the vertical circuit board is detachably inserted into a female connector securely mounted to the surface of a mainboard for electrical connection. The height of the circuit board can be set according to the requirement of a specific application of the memory card connector so that the space between the memory card connector and the mainboard can be properly utilized.

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